



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
YASUDA et al.)
Application Number: 10/654,983)
Filed: September 5, 2003)
For: SHARED MEMORY MULTIPROCESSOR)
PERFORMING CACHE COHERENCE)
CONTROL AND NODE CONTROLLER)
Attorney Docket No. ASAM.0092)
Unit 2111
Examiner
Ray, Gopal C.

**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

TERMINAL DISCLAIMER

Sir:

I, Juan Carlos A. Marquez, represent that I am the attorney of record for the above-identified application.

The invention has been assigned to Hitachi, Ltd., who is the owner by assignment of the entire right and title of the above-captioned patent application.

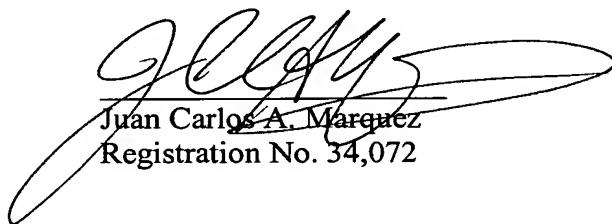
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Respectfully submitted,



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